According to ~~claims 1 and 2 of~~ the present invention, there is provided a dielectric ceramic composition and a multilayer electronic component which permit firing at a lower temperature than that of conventional forsterite, control to predetermined dielectric temperature characteristics, and multilayering without structural defects in designing a small, low-capacity multilayer electronic component, and which are capable of decreasing equivalent series resistance, suppressing variation in capacitance, and satisfying the characteristics in a range from CG to SL characteristics required for temperature compensation capacitors.

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Page 11, lines 14-23 ~~13-22~~:

~~For example,~~ as As shown in Fig. 1, a multilayer electronic component 1 (specifically, a multilayer ceramic capacitor) [[1]] according to the embodiment includes a laminate 4 including a plurality of stacked dielectric ceramic layers 2, and a plurality of first and second internal electrodes 3A and 3B disposed between the respective dielectric ceramic layers 2. Furthermore, first and second external electrodes 5A and 5B are formed at both end surfaces of the laminate 4 so as to be electrically connected to the first and second internal electrodes 3A and 3B, respectively.

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Page 13, line 3 ~~4~~ from the bottom to page 14, line 19 ~~18~~:

Samples Nos. 1 to 9 are dielectric ceramic compositions prepared for measuring the influence of the content a of strontium titanate by changing the Sr_yTiO_{2+y} molar ratio to Mg_xSiO_{2+x} (= $Sr_yTiO_{2+y}/Mg_xSiO_{2+x}$) from the range of the present invention to a value (a = 0.04 or ~~to~~ 0.42) outside the range of the present invention at the Mg/Si (= x) and Sr/Ti (= y) ratios set to 1.90 and 1.00, respectively, within the range of the present invention.

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AMENDMENTS TO THE SPECIFICATION

Page 3, lines 8-18:

The dielectric ceramic composition for high frequencies of Patent Document 1 can be sintered at a lower temperature than that of conventional forsterite (Mg_2SiO_4) and has a high Q factor and high dielectric constant. Therefore, the composition can be preferably used as a material for use in, for example, circuit element substrates used in the microwave band, such as microwave integrated circuits, and dielectric resonator supports. However, the composition has a firing temperature of as high as 1350°C to 1400°C and still has a problem in use as a multilayer capacitor material because of its high firing temperature.

Page 5, line 15 to page 6, line 1:

A dielectric ceramic composition ~~according to claim 1~~ of the present invention is represented by the general formula, $Mg_xSiO_{2+x} + aSr_yTiO_{2+y}$, wherein x, y and a satisfy the relations of $1.70 \leq x \leq 1.99$, $0.98 \leq y \leq 1.02$, and $0.05 \leq a \leq 0.40$, respectively.

A multilayer electronic component according to ~~claim 2 of~~ the present invention includes a laminate of a plurality of dielectric ceramic layers, internal electrodes disposed between the respective dielectric ceramic layers, and external electrodes electrically connected to the internal electrodes. The dielectric ceramic layers are formed using the dielectric ceramic composition of the invention ~~according to claim 1~~.

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Page 10, line ~~5~~ 4 from the bottom to page 11, line ~~7~~ 8.